## ABSTRACT

An arrangement for capturing data from a data stream of a predetermined data transfer rate by means of a flip-flop, comprises a symmetrical multi-phase clock generator that is adapted to be locked to a reference clock which in turn is adapted to generate a reference clock signal at the data transfer rate or at a fraction thereof, the multi-phase clock generator being adapted to generate n clock signals mutually shifted in phase 360°/n from each other, and a selector that is connected to the clock generator to receive the n clock signals, the selector being adapted to select one of these n clock signals as the system clock signal in response to a control signal from a clock phase counter, the clock phase counter being controlled to count up or down in response to the phase of the system clock signal when a predetermined number of data transitions have occurred in the data stream, said flip-flop being adapted to be controlled by the opposite phase of the system clock signal to capture said data from the data stream.

Fig. 1